

NAME

xr16 – xr16 instruction set assembler and simulator

SYNOPSIS

```
xr16 [-hex=hex-file] [-lst=lst-file]file* [-sim instruction-counts*
```

DESCRIPTION

xr16 is an assembler and a simulator for the xr16 instruction set architecture. It reads each assembly source *file* and assembles it into the image, resolving local addresses and global addresses across assembly source files.

If the option `-hex=hex-file` is given, xr16 writes a hexadecimal image file suitable for downloading.

If the option `-lst=lst-file` is given, xr16 writes a listing file.

Simulation is optional. If the `-sim` option is given, xr16 simulates some number of cycles. By default, or if the instruction count `-1` is given, xr16 runs the program until it exits by jumping to address 0.

If a series of instruction counts (e.g. `-sim l m n`) follow the `-sim` option, the simulator will run the first *l* instructions with tracing on, the next *m* with tracing off, the next *n* with tracing on again. To skip tracing of the first million instructions, then run indefinitely, try `xr16 file* -sim 0 1000000 -1`.

SEE ALSO

[The xr16 Specifications](#)

BUGS

See issues list, where `area` equals `xr16asm`.

OTHER

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